



L Number	Hits	Search Text	DB	Time stamp
1	11	704/275.ccls. and processors and pda and dsp and (speech	USPAT;	2003/09/24 20:35
	1	adj1 recognition) and (command instruction) and pda	US-PGPUB	
2	1	704/275.ccls. and (CPU (maim adj1 processor)) and dsp and	USPAT;	2003/09/24 20:38
		(command) and pda and (speech adj1 recognition)	US-PGPUB	
l - I	19	(motherboard and computer and (cpu microprocessor	USPAT;	2003/04/17 21:20
		processor) and dsp and memory) and ((spoken voice	US-PGPUB;	
		speech) adj1 command)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	33	(motherboard and computer and (cpu microprocessor	USPAT;	2003/04/17 21:20
		processor) and dsp and memory) and ((spoken voice speech	US-PGPUB	
		audio) adj1 command)	EPO; JPO;	
1		, ,	DERWENT;	
			IBM_TDB	
-	16	(motherboard and computer and (cpu microprocessor	USPAT;	2003/04/17 21:21
		processor) and dsp and memory) and ((spoken voice speech	US-PGPUB;	
		audio) adj1 command) and pci	EPO; JPO;	
			DERWENT;	
			IBM TDB	
-	13	(motherboard and computer and (cpu microprocessor	USPAT:	2003/04/17 21:24
1		processor) and dsp and memory) and ((spoken voice speech	US-PGPUB;	
		audio) adj1 command) and pci and bus and power and input	EPO; JPO;	
		,,,	DERWENT;	
			IBM_TDB	
-	3	(motherboard and computer and (cpu microprocessor	USPAT:	2003/04/17 21:24
	_	processor) and dsp and memory) and ((spoken voice	US-PGPUB;	
		speech) adj1 command) and pci and bus and power and	EPO; JPO;	
1		input	DERWENT;	
			IBM TDB	
-	38	(motherboard PCB) and computer and (cpu microprocessor	USPAT;	2003/04/17 21:56
1		processor) and dsp and memory and ((spoken speech verbal	US-PGPUB;	
1		voice audio) adj1 command)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	11	((motherboard PCB) and computer and (cpu microprocessor	USPAT;	2003/04/17 22:00
		processor) and dsp and memory and ((spoken speech verbal	US-PGPUB;	
		voice audio) adj1 command)) and 704/\$.ccls.	EPO; JPO;	
	1		DERWENT;	
			IBM_TDB	
-	10	((motherboard PCB) and computer and (cpu microprocessor	USPAT;	2003/04/17 21:54
		processor) and dsp and memory and ((spoken speech verbal	US-PGPUB;	
l i	!	voice audio) adj1 command)) and 704/\$.ccls. and bus	EPO; JPO;	
			DERWENT;	
	ļ		IBM_TDB	
-	13	((motherboard PCB) and computer and (cpu microprocessor	USPAT;	2003/04/17 21:52
		processor) and dsp and memory and ((spoken speech verbal	US-PGPUB;	
		voice audio) adj1 command)) and pci and power	EPO; JPO;	
	ļ		DERWENT;	
	'	l <u></u>	IBM_TDB	
-	47	(motherboard PCB (main adj1 circuit adj1 board) backplane)	USPAT;	2003/04/17 21:58
		and computer and (cpu microprocessor processor) and dsp	US-PGPUB;	
		and memory and ((spoken speech verbal voice audio) adj1	EPO; JPO;	
	ļ	command)	DERWENT;	
			IBM_TDB	000010414040
-	50	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:02
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.	IBM_TDB	2002/04/47 00:00
-	1	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/17 22:02
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
L	<u> </u>	704/\$.ccls. and pci	IBM_TDB	L



-	24	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/17 22:10
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and pci	DERWENT;	
		and (memory same dsp)	IBM_TDB	
-	20	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 20:54
Į.		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
}		((spoken speech verbal voice audio) adj1 command)) and pci	DERWENT;	
		and (memory same dsp) and power	IBM_TDB	
-	32	(((motherboard mother-board (mother board) PCB (main	USPAT;	2003/04/17 22:11
		adj1 circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and (memory same dsp)	IBM_TDB	
-	23	(((motherboard mother-board (mother board) PCB (main	USPAT;	2003/04/17 22:19
		adj1 circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and bus and (memory same dsp) and power	IBM_TDB	0000004447.00.45
-	1	"05844824"	USPAT;	2003/04/17 22:19
1			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	24	(//matharhaard mathar board / mathar board) BCB (main	IBM_TDB USPAT;	2003/04/17 22:21
-	24	(((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	2003/04/1/ 22.21
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and bus and (memory same dsp)	IBM_TDB	
1_	50	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 10:41
	"	circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	2003/04/10 10.41
	ĺ	microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and	DERWENT;	
		(bus pci)) and pci	IBM_TDB	
-	34	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 10:42
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and	DERWENT;	
		(bus pci)) and pci and speech	IBM_TDB	
-	29	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:06
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and	DERWENT;	
	_ [(bus pci)) and pci and (speech adj1 recognit\$4)	IBM_TDB	
-	20	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 15:57
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and	DERWENT;	
	40	(bus pci)) and pci and (speech adj1 recognit\$4) and power	IBM_TDB	2002/04/40 40:05
-	19	(((motherboard mother-board (mother board) PCB (main	USPAT;	2003/04/18 16:05
		adj1 circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command) and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and (bus pci)) and pci and (speech adj1 recognit\$4) and power)	DERWENT; IBM_TDB	
		and dynamic\$5 and command	יסמי_ואסי	
_	1	(((motherboard mother-board (mother board) PCB (main	USPAT;	2003/04/18 16:00
	'	adj1 circuit adj1 board) backplane) and computer and (cpu	US-PGPUB:	2000/04/10 10:00
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and	DERWENT;	
		(bus pci)) and pci and (speech adj1 recognit\$4) and power)	IBM_TDB	
		and dynamic\$5 and command and phoneme	.500	
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-	50	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu	USPAT; US-PGPUB;	2003/04/18 16:15
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.	IBM TDB	
-	42	(((motherboard mother-board (mother board) PCB (main	USPAT;	2003/04/18 16:17
		adj1 circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and vocabulary and ((speech voice) adj1	IBM_TDB	
		recognition)		
-	29	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:15
		circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and	US-PGPUB; EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and	DERWENT;	
		(bus pci)) and pci and (speech adj1 recogni\$4)	IBM_TDB	
_	32	(((motherboard mother-board (mother board) PCB (main	USPAT;	2003/04/18 16:18
		adj1 circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
1		704/\$.ccls.) and vocabulary and ((speech voice) adj1	IBM_TDB	
1	,	recognition) and bus	LICDAT:	2002/04/40 40 40
-	11	(((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu	USPAT; US-PGPUB;	2003/04/18 18:19
		microprocessor processor) and dsp and memory and	EPO; JPO;	
1		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and vocabulary and ((speech voice) adj1	IBM_TDB	
	!	recognition) and bus and phoneme and (continuous adj1	_	
		speech) and (voice adj1 command)		
-	20	(((motherboard mother-board (mother board) PCB (main	USPAT;	2003/04/18 20:50
		adj1 circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1	DERWENT; IBM_TDB	
	1	recognition) and bus and phoneme	IDM_IDS	
_	6	"06154830" "06085314"	USPAT;	2003/04/18 18:24
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	ا ما	10647004011	IBM_TDB	0000/04/40 40:04
-	2	"06170049"	USPAT; US-PGPUB;	2003/04/18 18:24
		i	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	20	(((motherboard mother-board (mother board) PCB) and	USPAT;	2003/04/18 20:53
		computer and (cpu microprocessor processor) and dsp and	US-PGPUB;	
		memory and ((spoken speech verbal voice audio) adj1	EPO; JPO;	
		command)) and 704/\$.ccls.) and vocabulary and ((speech	DERWENT;	
1_	10	voice) adj1 recognition) and bus and phoneme (((motherboard mother-board) and computer and (cpu	IBM_TDB USPAT;	2003/04/18 20:51
] -	'0	microprocessor processor) and dsp and memory and	US-PGPUB;	2000/04/10 20:01
1		((spoken speech verbal voice audio) adj1 command)) and	EPO; JPO;	
1		704/\$.ccls.) and vocabulary and ((speech voice) adj1	DERWENT;	
1		recognition) and bus and phoneme	IBM_TDB	
-	20	((((mother board) PCB) and computer and (cpu	USPAT;	2003/04/18 20:53
		microprocessor processor) and dsp and memory and	US-PGPUB;	
		((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1	EPO; JPO; DERWENT;	
		recognition) and bus and phoneme	IBM_TDB	
-	11	((motherboard mother-board (mother adj1 board) PCB (main	USPAT;	2003/04/18 20:55
		adj1 circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
1		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and pci	DERWENT;	
	<u> </u>	and (memory same dsp) and power	IBM_TDB	

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S7					
- 36 PCI and PDA and dsp and cpu and ((speech voice) adj1 cscognition) PCI and ((cellular mobile) adj1 phone) and (speech adj1 US-PGPUB US	-	87	PCI and PDA and dsp and cpu		2003/04/18 21:28
PCI and ((céllular mobile) adj1 phone) and (speech adj1 recognition) and dsp PCI and ((céllular mobile) adj1 phone) and vocabulary US-PGPUB US-PAT	-	36	PCI and PDA and dsp and cpu and ((speech voice) adj1	USPAT;	2003/04/18 22:42
Comparison Com		_			
- 3 PCI and ((cellular mobile) adj1 phone) and vocabulary USPAT; US-PGPUB	-	9			2003/04/18 22:55
US-PGPUB	_	3			2003/04/18 22:56
Phone) and DSP and (noise adj1 cancel\$6)			, , , , , , , , , , , , , , , , , , , ,		
- 4 (dsp (dsp adj1 chip)) and (menu near1 select\$) and (voice adj1 dialing) - 7 (dsp (dsp adj1 chip)) and (menu near1 select\$) and (voice adj1 dialing) - 7 (dsp (dsp adj1 chip)) and (menu near1 select\$) and (voice adj1 dialing) - 8 (menu near1 select\$) and (voice adj1 dialing) and (noise adj1 US-PGPUB, IBM, TDB USPAT; US-PGPUB, IBM, TDB USPAT; US-PGPUB, IBM, TDB USPAT; US-PGPUB, IBM, TDB USPAT; US-PGPUB, EPO, JPO, DERWENT; IBM, TDB USPAT; U	-	46			2003/04/19 20:11
- 4 (dsp (dsp adj1 chip)) and (menu near1 select\$) and (voice adj1 dialing) - 7 (dsp (dsp adj1 chip)) and (menu near1 select\$) and (voice adj1 dialing) - 7 (dsp (dsp adj1 chip)) and (menu near1 select\$) and (voice adj1 dialing) - 0 (menu near1 select\$) and (voice adj1 dialing) and (noise adj1 libm_TDB length libm_TDB			phone)) and DSP and (noise adj1 cancel\$6)		
-					
adj1 dialing) (dsp (dsp adj1 chip)) and (menu near1 select\$) and (voice adj1 dialing) US-PGPUB (US-PGPUB EPO, JPO, DERWENT; IBM_TDB USPAT; US-PGPUB;				· ·	
-	-	4			2003/04/19 21:03
adj1 dialing) adj1 dialing) (menu near1 select\$) and (voice adj1 dialing) and (noise adj1 US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; USPGPUB; EPO; JPO; DERWENT	_	7			2003/04/19 21:05
Common near 1 select\$) and (voice adj1 dialing) and (noise adj1 DB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO		•			2000/04/10 21:00
- 0 (menu near1 select\$) and (voice adj1 dialing) and (noise adj1 USPAT; USPGPUB; EPO; JPO; DERWENT; IBM, TDB			, ,		
-					
Cancellation Canc	_	n	(menu near1 select\$) and (voice adi1 dialing) and (noise adi1		2003/04/19 21:29
Solution Comparison Compa		J			2000/04/10 21:20
13					
5					
Select\$) Select\$ US-PGPUB;	_	5	(voice adi1 dialing) and (dsp (dsp adi1 chip)) and (menu adi1		2003/04/19 22:54
- 5 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu adj1 select\$) - 13 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu) - 13 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu) - 20 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 1 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 recognition) - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 - 2003/04/19 23:14	1	•		•	
- 5 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu adj1 select\$) - 13 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu) - 13 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu) - 20 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 1 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat) - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat) - 2003/04/19 23:10					
- 5 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu adj1 select\$) - 13 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu) - 13 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu) - 20 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 1 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 - 2003/04/19 23:10 2003/04/19 23:10 2003/04/19 23:11				·	
adj1 select\$) adj1 select\$) (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu) (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition) (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition) ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) ((noise adj1 cancel\$5) same (dsp (dsp adj1 chip)) and (speech adj1 uspAT; adj1 recognition)) ((noise adj1 cancel\$5) same (dsp (dsp adj1 chip)) and (speech adj1 uspAT; adj1 adj1 adj1 adj1 adj1 adj1 adj1 adj1	-	5	(noise adi1 cancel\$5) and (dsp (dsp adi1 chip)) and (menu	_	2003/04/19 23:05
- 13 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu) US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; I		-			
- 13 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu) USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; U					
- 20 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu) - 20 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 - 2003/04/19 23:10 2003/04/19 23:10 2003/04/19 23:10 2003/04/19 23:10	1			· · · · · · · · · · · · · · · · · · ·	
- 20 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 7 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 8 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 9 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 2 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 2 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 2 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 2 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 2 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 3 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 3 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 3 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 3 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 3 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 4 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 5 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 5 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 6 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 7 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 7 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 7 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 7 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 7 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 7 (noise adj1 cancel\$5) same (dsp) and (speech adj1 uspat; - 7 (noise adj1 cancel\$5) same (ds	-	13	(noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu)		2003/04/19 23:10
- 20 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 - 2003/04/19 23:14					
- 20 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 - 2003/04/19 23:14	ļ				
- 20 (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 recognition) - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 recognition) - 2003/04/19 23:14					
- 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 USPAT; USPAT; USPAT; IBM_TDB USPAT;	_	20		USPAT;	2003/04/19 23:14
- 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 USPAT; BM_TDB USPAT; BM_TDB USPAT; BM_TDB USPAT; 2003/04/19 23:14			adj1 recognition)		
- 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) and (dsp same (speech adj1 recognition)) US-PGPUB; EPO; JPO; DERWENT; IBM_TDB - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 USPAT; 2003/04/19 23:14					
- 5 ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; Unoise adj1 cancel\$5) same (dsp) and (speech adj1 USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; 2003/04/19 23:14					
EPO; JPO; DERWENT; IBM_TDB - 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 USPAT; 2003/04/19 23:14	-	5		USPAT;	2003/04/19 23:13
DERWENT; IBM_TDB USPAT; 2003/04/19 23:14			adj1 recognition)) and (dsp same (speech adj1 recognition))		
- 1 (noise adj1 cancel\$5) same (dsp) and (speech adj1 USPAT; 2003/04/19 23:14					
I recognition) LUS-PGPUR: 1	-	1		•	2003/04/19 23:14
EPO; JPO;			recognition)		
DERWENT;					
IBM_TDB				IBM_TDB	
- 2 6182203.URPN. USPAT 2003/04/26 13:26 - 0 6539467.URPN. USPAT 2003/04/26 13:28	-			1	
- 0 6539467.URPN. USPAT 2003/04/26 13:28 - 0 6453405.URPN. USPAT 2003/04/26 13:28	-				
- 17 ("4814976" "5072418" "5329471" "55353331" "5617543" USPAT 2003/04/26 13:28	-		("4814976" "5072418" "5329471" "5535331" "5617543"	1	
"5655098" "5752273" "5864703" "6073228" "6167466"					
"6182203" "6209082" "6219773" "6260137" "6266686"					
- 0 704/\$.ccls. and CPU and dsp and (command adj1 mode) and USPAT; 2003/09/22 23:09	-	0		USPAT;	2003/09/22 23:09
pda US-PGPUB			pda		





-	8	704/\$.ccls. and CPU and dsp and (command) and pda	USPAT; US-PGPUB	2003/09/22 22:51
-	5	704/\$.ccls. and CPU and dsp and (command and mode) and pda	USPAT; US-PGPUB	2003/09/22 23:10
-	5	704/\$.ccls. and (CPU (maim adj1 processor)) and dsp and (command and mode) and pda	USPAT; US-PGPUB	2003/09/22 23:12
-	1	704/231.ccls. and (CPU (maim adj1 processor)) and dsp and (command) and pda	USPAT; US-PGPUB	2003/09/22 23:13
-	13	704/\$.ccls. and (CPU (maim adj1 processor)) and dsp and (command) and pda and (speech adj1 recognition)	USPAT; US-PGPUB	2003/09/24 20:37
-	50	704/\$.ccls. and motorola and dsp and (command instruction) and (speech adj1 recognition)	USPAT; US-PGPUB	2003/09/23 10:15
-	1	5873062.URPN.	USPAT	2003/09/23 09:59
-	1	"5640490".PN.	USPAT	2003/09/23 10:00
-	31	5640490.URPN.	USPAT	2003/09/23 10:00
-	25	("3581192" "3703609" "3838217" "3938394" "3940565" "3969972" "4181813" "4452079" "4658252" "4780906" "4817154" "4852170" "4862503" "4975957" "4991216" "4998280" "5027410" "5065432" "5068900" "5091948" "5121434" "5166981" "5202926" "5299125"	USPAT	2003/09/23 10:08
		"5321608").PN.		
-	1	704/\$.ccls. and dedicat\$ near (speech adj1 processor)	USPAT; US-PGPUB	2003/09/23 11:02
_	1	6230138.URPN.	USPAT	2003/09/23 10:18
-	1	6230138.URPN.	USPAT	2003/09/23 10:18
-	42	704/\$.ccls. and (two adj1 processors)	USPAT; US-PGPUB	2003/09/23 11:03
-	21	704/\$.ccls. and (two adj1 processors) and dsp	USPAT; US-PGPUB	2003/09/23 11:36
-	1	704/\$.ccls. and (two adj1 processors) and dsp and pda	USPAT; US-PGPUB	2003/09/23 11:04
-	2	704/\$.ccls. and (two adj1 processors) and pda	USPAT; US-PGPUB	2003/09/23 11:04
-	3	704/\$.ccls. and (two adj1 processors) and pda	USPAT; US-PGPUB	2003/09/23 11:05
-	2	704/\$.ccls. and (two adj1 processors) and pda	USPAT; US-PGPUB	2003/09/23 11:36
-	284	704/\$.ccls. and processors and pda	USPAT; US-PGPUB	2003/09/23 11:37
-	63	704/\$.ccls. and processors and pda and dsp	USPAT; US-PGPUB	2003/09/23 11:37
-	43	adj1 recognition) and (command instruction) and pda	USPAT; US-PGPUB	2003/09/24 20:35
-	23	704/\$.ccls. and processors and pda and dsp and (speech adj1 recognition) and (command instruction) and bus	USPAT; US-PGPUB	2003/09/23 11:54
-	3	704/\$.ccls. and processors and pda and dsp and (speech adj1 recognition) and (command instruction) and bus and cpu	USPAT; US-PGPUB	2003/09/23 11:56
-	70	processors and pda and dsp and (speech adj1 recognition) and (command instruction) and bus and cpu	USPAT; US-PGPUB	2003/09/24 18:15
-	5	processors and pda and dsp and (speech adj1 recognition) and (command instruction) and bus and cpu and (reduce	USPAT; US-PGPUB	2003/09/23 12:00
-	6	adj2 (power consumption)) processors and pda and dsp and (speech adj1 recognition) and (command instruction) and bus and cpu and ((power adj1 saving) (reduc\$ adj1 power))	USPAT; US-PGPUB	2003/09/24 18:18
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